

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:
Applicant:
Filed:
Title:
TC/A.U.:
Examiner:

10/634,089
H. Kanzawa et al.
August 4, 2003
METHOD OF MANUFACTURING CIRCUIT BOARD AND
COMMUNICATION APPLIANCE
3726

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98 and to the duty of disclosure set forth in 37 C.F.R. § 1.56, the Examiner in charge of the above-identified application is requested to consider and make of record the references listed on the PTO 1449 (R&P) submitted herewith.

Although the information submitted herewith may be "material" to the Examiner's consideration of the subject application, this submission is not intended to constitute an admission that such information is "prior art" as to the claimed invention.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

The following references are not in the English language:

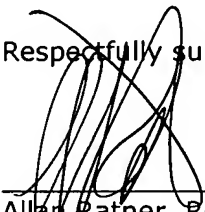
- 1) JP 2002-204049
- 2) JP 10-107445

Reference 1) includes an English language abstract. Also, reference 1) includes a "concise explanation" under 37 CFR §1.98(a)(3) as set forth in the background portion of the specification at page 4, line 24.

References 1) and 2) include U.S. corresponding patents.

This Information Disclosure Statement is being filed before receipt of the first Official Action and it is presumed that none has yet been mailed. No fee or statement is required. 37 C.F.R. §1.97(b).

Respectfully submitted,



Allan Ratner, Reg. No. 19,717
Attorney for Applicants

AR/ds

Enclosures: PTO Form 1449
(2) References

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Dated: December 8, 2003

The Commissioner for Patents is hereby authorized to charge payment to Deposit Account No. **18-0350** of any fees associated with this communication.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on:

December 8, 2003





(AW 08/03)

Substitute for Form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

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|----------------------|----------------------|
| Application Number | 10/634,089 |
| Filing Date | August 4, 2003 |
| First Named Inventor | Hideo Kanzawa, et al |
| Art Unit | 3726 |
| Examiner Name | |
| Attorney Docket No. | MTS-3455us |

SHEET 1 of 1

U.S. PATENT DOCUMENTS

| Examiner Initials* | Cite No. ¹ | Document Number | Issue Date/ Publication Date (MM-DD-YYYY) | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
|-----------------------|--------------------------|--|---|--|---|
| | | Number - Kind Code ² (if known) | | | |
| | | US-2003/0102153 (counterpart of JP2002-204049) | 06/05/2003 | Sugaya et al | Entire Document |
| | | US-2001/0023779 (counterpart of JP2002-204049) | 09/27/2001 | Sugaya et al | Entire Document |
| | | US-6,143,116 (counterpart of JP 10-107445) | 11/07/2000 | Hayashi, et al | Entire Document |
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FOREIGN PATENT DOCUMENTS

| Examiner Initials* | Cite No. ¹ | Foreign Patent Document | Publication Date (MM-DD-YYYY) | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | T ⁶ |
|-----------------------|--------------------------|--|----------------------------------|---|--|--------------------------|
| | | Country Code ³ - Number ⁴ - Kind Code ⁵ (if known) | | | | |
| | | JP 2002-204049 (counterpart of US 2003/0102153 and US 2001/0023779 w/English Abstract) | 07/19/2002 | Matsushita Electric Ind Co Ltd | Entire Document | <input type="checkbox"/> |
| | | JP 10-107445 (counterpart of US 6,143,116 w/English abstract) | 04/24/1998 | Kyocera Corp | Entire Document | <input type="checkbox"/> |
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| Examiner Signature | | Date Considered | |
|-----------------------|--|--------------------|--|

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

¹Applicant's unique citation designation number (optional).

²See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.

³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3).

⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible.

⁶Applicant is to place a check mark here if English language translation is attached.

The collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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